The Atheros Chipset for 108Mb/s Wireless LANs

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Atheros Communications, Inc. | www.atheros.com
WLAN Tutorial
- Market & Regulatory
- Standards
  - Modulation overview
  - MAC overview (QoS)
  - Throughput and system capacity
  - Security

Chipset Overview
- Client MAC/Baseband chip
- 2.4 GHz and 5 GHz analog chips
- Integrated Access Point chip
- Measured performance
WLAN is rapidly growing and shifting towards 5 GHz/802.11a

- 2002 InStat market report predicts (millions of units):

<table>
<thead>
<tr>
<th>WLAN Type</th>
<th>2002</th>
<th>2004</th>
<th>2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>802.11b (2.4GHz)</td>
<td>12.8</td>
<td>25.2</td>
<td>23.6</td>
</tr>
<tr>
<td>802.11a (5GHz)</td>
<td>0.7</td>
<td>6.5</td>
<td>16.3</td>
</tr>
<tr>
<td>Dual mode, dual band</td>
<td>0.7</td>
<td>7.0</td>
<td>29.3</td>
</tr>
</tbody>
</table>

- Laptops are currently the primary driver
- Future drivers include
  - Hotspot/WAN access (cell phones / PDAs)
  - Home/consumer electronics, particularly digital video
Unlicensed spectrum for WLAN has been allocated on a country/ regional basis

- 2.4-2.483 GHz (83 MHz total) has been available in most counties for many years
- Many countries have created much larger allocations at 5 GHz in the past few years:

<table>
<thead>
<tr>
<th>Region</th>
<th>5.15-5.25</th>
<th>5.25-5.35</th>
<th>5.47-5.725</th>
<th>5.725-5.825</th>
<th>Total (MHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>USA</td>
<td>200mW</td>
<td>1W</td>
<td>4W</td>
<td></td>
<td>300</td>
</tr>
<tr>
<td>Europe</td>
<td>200mW</td>
<td>200mW</td>
<td>1W</td>
<td></td>
<td>455</td>
</tr>
<tr>
<td>Japan</td>
<td>200mW</td>
<td></td>
<td></td>
<td></td>
<td>100</td>
</tr>
<tr>
<td>Asia Pacific, including Korea, Hong Kong, China, Singapore, New Zealand, Australia</td>
<td>200mW (Singapore, Australia, NZ)</td>
<td>200mW (Singapore, Australia, NZ, Taiwan)</td>
<td>100mW to 1W (all, at various power levels)</td>
<td>300</td>
<td></td>
</tr>
</tbody>
</table>

More spectrum = higher data rates and larger overall system capacity

- Radio is fundamentally a shared medium
- Current 2.4 GHz systems are limited to only 3 independent 11 Mb/s channels
- Current 5 GHz systems have >13 54 Mb/s channels
Currently, the only WLAN standards that matter are created in IEEE 802.11. Work is ongoing on numerous enhancements:

- **802.11**: Original standard provided for 1 and 2 Mb/s PHY layer, CSMA/CA MAC, adopted in 1997
- **802.11a**: enhancement to provide 54Mb/s in the 5 GHz band, adopted 1999
- **802.11b**: enhancement to provide 11Mb/s in the 2.4 GHz band, adopted 1999
- **802.11d**: Changes for international regulatory compliance, adopted 2001
- **802.11e**: Enhancements to the MAC to provide QoS through prioritized CSMA and advanced polling techniques, still in debate
- **802.11f**: Recommended practices for Inter-access point communication, nearly completed
- **802.11g**: PHY layer enhancement to provide 54Mb/s in the 2.4GHz band, still in debate
- **802.11h**: Enhancements to 802.11a to help with regulatory compliance in Europe, still in debate
- **802.11i**: Enhancements for greater security, still in debate
Not really CDMA, only one station in a cell transmits at a time

Spreading done to meet regulatory rules, and provide some added robustness to multi-path (echoes)

Code expansion is small, and code is not optimal. Coding gain is limited to ~2 dB.

Equalizer is required to deal with multi-path, computational load is proportional to the square of the data rate
Data rate on each carrier is very low; multi-path echoes are short compared to the symbol time. No time domain equalizer is required.

The iFFT and FFT are very efficient. The computational load is proportional to \((R/2)\log_2(R)\), where \(R\) is the data rate (compared to \(R^2\) for an equalized system).

A strong convolutional code (½ rate or ¾ rate) creates redundancy across the carriers that resists interference and frequency selective channels (coding gain >5dB).

Orthogonal spacing of the carriers allows a high data rate in a narrow channel bandwidth (54Mb/s in a 20 MHz channel).

Atheros Turbo mode doubles the data rate (and consumed bandwidth), providing 108Mb/s in 40MHz channels.
OFDM (52 carrier signals)

Each carrier is modulated with data according to one of:

- **BPSK**
  - 6 Mb/s (1/2 rate code)
  - 9 Mb/s (3/4 rate code)

- **QPSK**
  - 12 Mb/s (1/2 rate code)
  - 18 Mb/s (3/4 rate code)

- **16QAM**
  - 24 Mb/s (1/2 rate code)
  - 36 Mb/s (3/4 rate code)

- **64QAM**
  - 48 Mb/s (2/3 rate code)
  - 54 Mb/s (3/4 rate code)

Lower data rates support longer range.
Radios automatically change data rates as required to maintain connection.

Different data per tone (via FFT)
Multipath just scales tones
Tones remain orthogonal even with multipath

As transmitted
As received after echoes in channel
It is more expensive to build radios that transmit and receive at the same time. Therefore it is hard to detect collisions.

Normal packet loss rates are much higher in radio than in wired systems.

Use of ARQ (Acknowledgements) at the PHY layer greatly increases robustness, but degrades throughput.
EDCF has 8 counters and 8 pairs of (CW Min, CW Max) values, one for each of the 802.1p Traffic Class (TC) priority levels. TC 0 is lowest priority, TC 7 is highest.

High-priority >> small window >> greater access

Exemplifies the IETF concept of Differentiated Service (DIFFSERV).
AIFS value for each priority class extends the IFS spacing. Timeslot counting is delayed by the AIFS amount.

AIFS=1 is shown in the picture.
EDCF (3)

- EDCF defines three “knobs”
  - CW Min adjusts contention algorithm
  - CW Max bounds retransmission backoff window
  - AIFS inhibits access by traffic class
- 802.11e draft defines default values
- Defaults may be overwritten by AP via Beacon
- Effects
  - small AIFS values have large effect on latency
  - CW Min values control contention probability
    - adjust for small, medium, large number of stations within TC
    - adjust for gross differentiation between TCs
  - slow-acting adaptation to traffic is feasible
System Capacity is total **throughput** in a multi-cell deployment

\[
\text{System Capacity} = \text{Number of Cells} \times \text{Cell Throughput} \times \text{CCI Penalty}
\]

Co-Channel Interference (CCI) Penalty depends on:

- Number of Cells
- Cell Diameter
Higher System Capacity

- Large areas with 802.11a will suffer less Co-Channel Interference (CCI) than with 802.11b – resulting in higher system capacity
- Many cell systems can also include multi-story deployments
- Interference can come from other neighbors in multi-dwelling units
**Average Cell Throughput Comparison**

![Graph showing throughput comparison](image)

- **Blue Line**: 11a - 8 cell - no CCI
- **Red Dashed Line**: 11b - 3 cell - no CCI
- **Red Line**: 11b - 8 cell - CCI
### Throughput and System Capacity

<table>
<thead>
<tr>
<th>Mode</th>
<th>Raw data rate (Mb/s)</th>
<th>Atheros Turbo</th>
<th>11a &amp; 11b overlaid</th>
<th>11a</th>
<th>OFDM at 2.4GHz</th>
<th>50/50 mix 11g/11b 54 &amp; 11</th>
<th>11b</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single Cell throughput UDP</td>
<td>108</td>
<td>54.3</td>
<td>37.6</td>
<td>30.5</td>
<td>30.5</td>
<td>9.9</td>
<td>7.1</td>
</tr>
<tr>
<td>Single Cell TCP/IP</td>
<td>42.5</td>
<td>30.3</td>
<td>24.4</td>
<td>24.4</td>
<td>7.9</td>
<td>5.9</td>
<td></td>
</tr>
<tr>
<td># of Channels available</td>
<td>5 (US)</td>
<td>12/3 (US)</td>
<td>19/3 (EU)</td>
<td>12 (US)</td>
<td>19 (EU)</td>
<td>3</td>
<td>3</td>
</tr>
<tr>
<td>UDP System Capacity</td>
<td>212.5 (US)</td>
<td>387 (US)</td>
<td>366 (US)</td>
<td>366 (US)</td>
<td>580 (EU)</td>
<td>91.5</td>
<td>29.7</td>
</tr>
</tbody>
</table>

- All values in Mb/s, operation at the maximum PHY layer rate assumed
- 11b results assume short preamble mode
- Mixed 11b/11g assumes short preamble with legacy RTS/CTS
802.11 Security

Link Layer Encryption

- **WEP (Wired Equivalent Privacy):** The original simple encryption method, based on RC4. No longer considered secure since several attacks are known. Provides privacy only, no integrity check.
- **TKIP (Temporal Key Exchange Protocol):** A “fix” for WEP that scrambles the key between packets and adds a message integrity check to prevent spoofing. 802.11 considers this a temporary fix.
- **AES (Advanced Encryption Standard):** NIST standard symmetric block cipher intended to replace DES. It is considered to be “military strength” and includes an integrity check. This is the long term solution chosen by 802.11.

System Level Security

- **802.1x:** A general purpose and extensible framework for authenticating users and generating/distributing keys for encryption.
- **SSN/RSN (Simple Secure Network):** A recipe for authentication based on 802.1x, bridging between an 802.11 base station and an authentication server in the network.
Chipset Overview

PC or Laptop

- Host
- Mem.
- PCI Bridge
- CPU
- Driver
  (SW MAC)

- PCI Core
- ADC
- ADC
- DAC
- DAC
- Gain Control

- Transmitter
- Synthesizer
- Receiver
- Bias / Gain
  Control

- Balun & Match

Access Point or Gateway

- Mem. Controller
- NET PHY
- FLASH
- DRAM
- EEPROM
- ETH PHY
- UART
- GPIO
- ADC
- ADC
- DAC
- DAC
- Gain Control

- CPU
- MAC
- PHY

- Mem. Controller
- UART
- GPIO

- AR5311
- AR5211
- AR5111

OR

5 GHz
- Tx
- Rx

2.4 GHz
- Tx
- Rx

5 GHz
- Diplexer

2.4 GHz
- Diplexer

4. = Required for 5 and 2.4 GHz operation

5. = Required for 2.4 GHz operation
AR5211 Baseband IC

- Two 80 Ms/s 9 bit ADCs
- Two 176Ms/s 9 bit DACs
- 196 pin PBGA
- ~7 M transistors
- 0.25um CMOS
- 175 GOPs equivalent
- WEP, AES encryption in hardware
- QoS support via EDCF with 12 priority queues
- <250 mW
- 802.11a data rates from 6 to 54Mb/s
- Turbo data rates from 12 to 108Mb/s
- 11b data rates from 1 to 11Mb/s
Each QCU is associated with an individual stream of data
Each DCU is associated with a QoS priority level
The ARB chooses which packet goes next
The MAC generates and sends packets that are time critical: ACKs, CTS, etc., and handles the automatic retransmission of failed packets (no ACK received)
Digital PHY Tx internals

- $T_x$ data from MAC
- FSM
  - FEC
  - Scramble
  - Encode
  - Puncture
- Interleave
  - Map
  - Scale
- IFFT
- Short Training
- Upsample
  - FIR
- DAC
- Differential I and Q to Analog Front End

Pilots, Long Training
Digital PHY Rx internals

1. Differential I and Q from Analog Front End
2. ADC
3. Downsample FIR
4. Remove DC Offset
5. Rotate
6. To FFT
7. Auto Correlate
8. Frequency Lock
9. Signal Timing
10. Signal Detect and AGC
11. Rx Gain to Analog Front End
12. Pipeline Control
Digital PHY Rx Internals (2)

From Rotator

FFT → Channel Correct → De-Interleave → Viterbi Decoder → Rx Data to MAC

Channel Estimate and Tracking

Pipeline Control
- MAC and Digital PHY the same as AR5211
- 32-bit MIPS R4000 processor
- 16kB instruction cache
- 16kB data cache
- 1 Mb/s UART, can control Bluetooth radio
- Two Ethernet MACs for chaining with legacy AP or use as firewall device
- 32-bit SDRAM interface
- 16-bit FLASH interface
- Can support up to 4 memory devices
- Second FLASH interface can be used as local bus
- ~11M transistors, 0.25um CMOS
- 388 pin PBGA
On-chip transmit and receive filters
- Ultra-wide tuning range: 4.9-6 GHz
- On-chip Power Amplifier delivers 16dBm
- On-chip LNA with 5dB NF
- 64 pin QFN
- Standard 0.25um digital CMOS
AR2111 Frequency Converter

- Converts signals between 5 GHz and 2.4 GHz
- Can support 802.11b or OFDM in the 2.4 GHz band (802.11g)
- 48 pin QFN
- Standard 0.25um digital CMOS
802.11a provides 2 to 4.5 times the throughput of 802.11b at the same distance when tested to 225 feet.
The Atheros 5001X chipset provides:

- Two chip 802.11a client solution, three chips for 802.11a/802.11b dual mode
- Two chip 802.11a Access Point solution
- Raw data rates from 1 to 108 Mb/s
- “Military strength” AES encryption and security
- Priority based Quality of Service


More information about the Atheros chipset can be found at www.atheros.com