

Jim Handy Director, **Objective Analysis**

Jim Handy of Objective Analysis has over 35 years in the electronics industry including 20 years as a leading semiconductor and SSD industry analyst. Early in his career he held marketing and design positions at leading semiconductor suppliers including Intel, National Semiconductor, and Infineon. A frequent presenter at trade shows, Mr. Handy is known for his technical depth, accurate forecasts, widespread industry presence and volume of publication. He has written hundreds of market reports, articles for trade journals, and white papers, and is frequently interviewed and quoted in the electronics trade press and other media.

Krishna Parat Intel Fellow, Non-Volatile Memory Solutions Group **Intel**

Krishna Parat received a B. Tech. degree in electrical engineering from the Indian Institute of Technology, Chennai, India in 1985, and an M.S. and a Ph. D. degree from Rensselaer Polytechnic Institute, Troy, New York in 1987 and 1991 respectively. In 1991 Krishna joined the Flash Memory Development group at Intel where he is currently responsible for Intel's NAND Flash technology scaling.

Radoslav Danilak Co-founder and CEO **Skyera**

Dr. Danilak has over 20 years of experience and has filed more than 80 patents while architecting and designing state-of-the-art processing engines. Prior to Skyera Inc., Radoslav was founder and CTO of SandForce Inc., the first and currently only company that enabled MLC flash memory in enterprise storage. Radoslav was a senior chipset and graphics processor architect at NVIDIA, and prior to that, held senior engineering and architect positions at Nishan Systems, Toshiba, Gizmo Technologies and DanSoft. Radoslav earned his Ph.D in Computer Science and his MS in Electrical Engineering and Computer Science from the Technical University of Kosice, Slovak Republic.

Amber Huffman Senior Principal Engineer in the Storage Technologies Group **Intel**

Amber Huffman is a Senior Principal Engineer in the Storage Technologies Group at Intel where her responsibilities include storage performance and architecture for solid-state drives and cache technologies. Amber's current projects include delivering storage solutions for Ultrabook™ platforms and defining the host controller interface for enterprise and client PCI Express® SSDs solid-state drives. She leads several industry standards efforts and drives the resulting technology into product. Amber serves as the chairperson for the NVM Express (NVMe) Workgroup and chairs the Board of Directors of the Open NAND Flash Interface (ONFI) Workgroup that defines the NAND component level interface. She has been awarded 19 patents in storage architecture. Amber holds a BSE in Computer Engineering from the University of Michigan, an MSE from Stanford University in Electrical Engineering, and has been with Intel for 16 years.

Kevin Rowett

SVP, Engineering

Violin Memory

Kevin Rowett has over 20 years of progressive and responsible engineering and management experience, most recently as Senior Vice President of Engineering at Violin Memory in Mountain View, California. He graduated from Oklahoma University with a BS in Mathematics. Kevin has worked as an engineer, engineering team lead, engineering manager, and executive at many well-known Silicon Valley high technology companies including Tandem, IBM, and Cisco. He has developed proven – and respected – expertise in hardware/software systems design while working at such companies. He has also founded six startup high technology companies, including:

- Force10 Networks, which was sold to Dell for US \$768 million
- Mistletoe Technologies, which designed and produced a high speed security chip
- Violin Memory, a leading supplier of flash storage system to enterprise companies, currently in pre-IPO phase

Neil Vachharajani

Software Architect

Pure Storage

Neil Vachharajani is a software architect at Pure Storage. He has been involved in most aspects of the design and implementation of the Flash Array including data redundancy, high availability, space management, and performance. Prior to Pure Storage, Neil was a Senior Engineer on the compiler optimization team at Google where he worked on tools and infrastructure to improve the performance of backend data-center applications. Neil has a BSE in Electrical Engineering and Ph.D. in Computer Science from Princeton University.

John Davis

Researcher

Microsoft Research

John D. Davis has a Ph.D. from Stanford focusing on computer architecture. His research interests include computer architecture, large-scale computing systems, embedded systems, non-volatile memory, application behavior and performance tuning, and hardware-software co-design and interaction.

David Flynn

CEO

PrimaryData

Currently the CEO of PrimaryData a stealth mode enterprise storage company. Founder and former CEO of Fusion-io where he pioneered the use of Flash in the enterprise data-center. Architected several of the the worlds fastest supercomputers, leading the adoption of Infiniband and RDMA based architectures. Authored flight simulation software for a US Army missile system at the age of 17. Holds over 100 patents in areas ranging from web browser technologies, mobile device management, network switching and protocols to distributed storage systems.